# Research of Novel Three-phase Inverter with SPWM Technique

**Ali Khajezadeh, Hasan Mansouri**

**Abstract:** This paper proposes a novel three-phase uncontrollable rectifier inverter without or with a quite small dc-link capacitor. Because a modulation wave reconstruction-SPWM (MWR-SPWM) technique is adopted which can greatly eliminate the harmonics of output voltage, the DC filter capacitor is greatly decreased or even removed. In addition, the size of the input ac filter and the output ac filter is reduced. The converter which is like a half bridge topology, has high power flow capability and minimum device stresses that can suitably interface a super capacitor with the drive train of a hybrid electric vehicle. The principle of operation and harmonics elimination of the novel inverter topology are elaborated.

**Keywords:** inverter; MWR-SPWM; harmonics elimination

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## I. INTRODUCTION

Inverter are widely used in many industrial applications such as variable-frequency velocity modulation[1][2][3], UPS, VAR compensator etc. In order to supply high quality power for loads, it is significant for this inverter to eliminate harmonics in output voltage effectively. Pulse width modulation (PWM) technique that has satisfied performance in harmonics elimination, voltage regulation, responding speed is widely used in all kinds of inverters. [4][5]

Conventionally, all PWM-controlled inverters are based on ideal dc voltage, in practice a bulky dc filter capacitor is installed after the three-phase uncontrollable rectifier to obtain low-ripple dc voltage, as shown in Figure 1. However, the dc filter capacitor has several disadvantages from the viewpoints of size, weight, cost, and reliability[6]. Moreover, the properties of the dc filter capacitor deteriorate gradually

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due to continuous out-gassing. Hence, dc filter capacitor is the major factor limiting the lifetime of inverter systems [7].

In order to solve the above-mentioned problems, we propose a novel three-phase inverter based on modulation wave reconstruction - SPWM (MWR-SPWM) technique, as shown in Figure 2. Due to MWR-SPWM technique is adopted, the dc filter capacitor is decreased largely or even removed and the harmonics in output voltage is eliminated effectively. The main use of capacitor isn't filtering harmonics but buffering energy. When the power factor of load is high enough, the capacitor can be omitted. Meanwhile the size of input ac filter and the output ac filter are proportionately reduced.

## II. PRINCIPLE OF OPERATION

The novel inverter topology mainly consists of a three-phase diode bridge and an inverter. The output of the three-phase diode bridge rectifier $v_{dc}$ is a type of six-pulse dc voltage as shown in Figure 3, which contains inherent harmonics of the 6n is the system frequency) besides dc component. Using conventional SPWM technique the output voltage of inverter contains harmonics $f_{6k} = 6kf_0$ , $f_{\sin}$ is the modulation wave frequency) besides fundamental component in low frequency band.
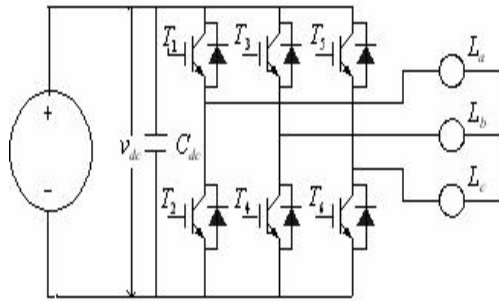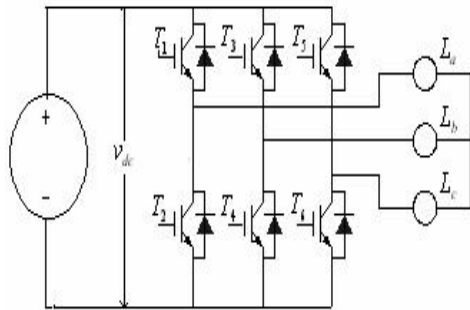
Figure 1. A conventional inverter



Figure 2. The proposed inverter

We can achieve this purpose by using modulation wave reconstruction-SPWM technique. Meanwhile the value and frequency regulation of the output voltage can be conventionally achieved by regulating modulation wave. In the next section, the detailed analysis is given.

## III. MODULATION WAVE RECONSTRUCTION - SPWM TECHNIQUE

### 1. Analysis of Output Voltage Using Conventional SPWM Technique

In this section, the proposed inverter is analyzed under steady-state condition.

In Figure 2, $v_{dc}$ is the output dc voltage of the three-phase diode bridge rectifier, we choose its midpoint as zero potential reference. In order to make the analysis comprehensive, the expression of $v_{dc}$ can be represented by the following Fourier series in (1).

$$v_{dc} = V_D + \sum_{i=1}^{\infty} U_i \cos\phi_i \cos w_i t + \sum_{i=1}^{\infty} U_i \sin\phi_i \sin w_i t \quad (1)$$

Firstly, we chose standard three-phase sinusoidal wave as modulation wave, take phase a for example, it's expressed as follows.

$$u_{s1} = U_B \sin(wt + \theta) \quad (2)$$

Figure 3 shows the relationship among triangular-wave voltage $u_{tr}$, modulation wave voltage $u_{s1}$ and voltage of a phase $u_a$. Switching status is determined by the compared result between $u_{s1}$ and $u_{tr}$ as follows: the switch turns on when $u_{s1} > u_{tr}$, but turns off when $u_{s1} < u_{tr}$. The frequency of triangular-wave $f_{tr}$ is M times to the frequency of sinusoidal modulation wave $f_{\sin}$. The sinusoidal wave is "chopped" 2 M times in per sinusoidal cycle. We get 2 M angles, define them as $\alpha_i$ ($i$ = 1, 2, ...., 2 M).
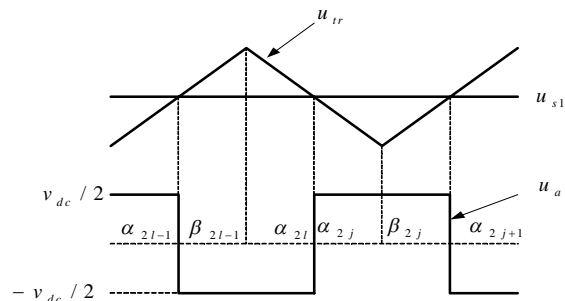


Figure 3. The relationship of $u_{tr}$, $u_{s1}$, $u_{a}$,

When (1) is modulated under (2), the Fourier series coefficients of output voltage $u_a$ are respectively expressed as $a_n^{(1,1)}$, $b_n^{(1,1)}$, $a_n^{(2,1)}$, $b_n^{(2,1)}$, $a_n^{(3,1)}$, $b_n^{(3,1)}$. The output voltage $u_a$ fluctuates between $v_{dc}/2$ and $-v_{dc}/2$, and

its Fourier series can be expressed as (3), given at the bottom of this page.

Since the frequency of the triangular wave is sufficiently high, we can believe that $u_{s1}$ and $v_{dc}$ is constant during one triangular-wave cycle.

Therefore

$$\frac{\alpha_{2j+1} + \alpha_{2j}}{2} \approx \beta_{2j} \qquad (4)$$

$$\frac{\alpha_{2l} + \alpha_{2l-1}}{2} \approx \beta_{2l-1} \qquad (5)$$

$$\frac{\alpha_{2j+1} - \alpha_{2j}}{2} \approx \frac{U_B \sin(\beta_{2j} + \theta) - V_{trl}}{V_{trm} - V_{trl}} \cdot \frac{\omega T_{tr}}{2} \qquad (6)$$

$$\frac{\alpha_{2l} - \alpha_{2l-1}}{2} \approx \frac{V_{trm} - U_B \sin(\beta_{2l-1} + \theta)}{V_{trm} - V_{trl}} \cdot \frac{\omega T_{tr}}{2} \qquad (7)$$

Substituting (11), (12), (17), (18), (19), (20) into (3), we can obtain (21). Obviously, $u_a$ contains of a fundamental component, lower harmonics of $f_k \pm f_{\sin}$ ($k = 1, 2 \cdots$, $f_k = w_k / 2\pi$, $f_{\sin} = w / 2\pi$), and harmonics above the switching frequency generated by triangular-wave SPWM operation. Then there is a relationship between $u_a$ and $v_{dc}$ which is expressed as follows:

$$u_a = T(\theta) \cdot v_{dc} \qquad (22)$$

Where $T(\theta)$ is the switching function of triggering pulses. Substituting the second step in (21) into (22), we can get the following:

$$T(\theta) = \frac{U_B \sin(\omega t + \theta)}{V_{trm} - V_{trl}} \qquad (23)$$

$$\sin(n \frac{\alpha_{2j+1} - \alpha_{2j}}{2}) \approx n \frac{\alpha_{2j+1} - \alpha_{2j}}{2} \qquad (8)$$

$$\sin(n \frac{\alpha_{2l} - \alpha_{2l-1}}{2}) \approx n \frac{\alpha_{2l} - \alpha_{2l-1}}{2} \qquad (9)$$

$a_n^{(1,1)}$ is expressed as (10), given at the bottom of this page.

Substituting (4)--(9) into (10), we can obtain (11).

Because $wT_{tr}$ is so small, we can convert the form of the first step in (11) into an integral one, which simplifies the result. $b_n^{(1,1)}$ can be derived by using the same method.

## 2. Analysis of Output Voltage Using MWR-SPWM Technique

From above analysis, we can add some component to standard modulation wave to offset the influence on output ac voltage from dc voltage's fluctuation. Based on this idea, we propose a new modulation wave as follows:

$$u_s = k + u_{s1} \qquad (24)$$

We consider that $u_s$ consists of two independent parts, one is $u_{s1}$, another is $k$. When (24) is chosen as modulation wave, the switching function $T(\theta)$ can be expressed as follows:

$$T(\theta) = T'(\theta) + T''(\theta) \qquad (25)$$

Where $T'(\theta)$ is the component of switching function when modulation wave is $u_{s1}$ which is shown in (23);

$T^{''}(\theta)$ is the component of switching function when modulation wave is $k$.

When the new modulation wave is adopted we hope

$$u_a = (T^{'}(\theta) + T^{''}(\theta)) \cdot (U_D + \sum_{k=1}^{\infty} U_k \cos(\omega_k t - \phi_k)) = T^{'}(\theta) U_D \quad (26)$$

$$T^{''}(\theta) = -\frac{\sum_{k=1}^{\infty} U_k \cos(\omega_k t - \phi_k)}{U_D + \sum_{k=1}^{\infty} U_k \cos(\omega_k t - \phi_k)} \cdot T^{'}(\theta) = -\frac{v_{dc} - U_D}{v_{dc}} T^{'}(\theta) \quad (27)$$

Substituting (23) into (27), we obtain the following:

$$T^{''}(\theta) = -\frac{v_{dc} - U_D}{v_{dc}} \cdot \frac{U_B \sin(\omega t + \theta)}{V_{trm} - V_{trl}} \quad (28)$$

$$k = -\frac{v_{dc} - U_D}{v_{dc}} u_{s1} \quad (29)$$

$$u_a \approx \frac{U_D U_B \sin(\omega t + \theta)}{V_{trm} - V_{trl}} \quad (30)$$

□. SIMULINK AND EXPERIMENTAL RESULT

In order to verify the analyses and the simulation results, a 2-kVA experimental novel inverter that adopts above proposed topology was set up in laboratory. The diode bridge rectifier is connected with a three-phase unbalanced supply. The circuit parameters used in experiment is the same with the ones in simulation.

that the harmonics of $u_a$ can be eliminated effectively, we can obtain (26), given at the bottom of this page. Therefore, we can obtain (27).

When $T^{''}(\theta)$ is compared with $T^{'}(\theta)$, $k$ can be obtained by homogeneity theorem.

Therefore, when the new modulation wave $u_s$ is adopted $u_a$ only contains fundamental component in low frequency band.

So, when power system is balanced system, new modulation wave $u_s$ is adopted that can eliminate the influence of $6nf_0$ harmonics from three-phase diode bridge output voltage to harmonics of converter output voltage so as to not use bulky dc filter capacitor.

Figure 4 and Figure 5 show the load phase current waveforms with general SPWM and MWR-SPWM technique respectively. Figure 6 and Figure 7 show the output voltage spectrum by using general SPWM and MWR-SPWM technique respectively. Obviously, the novel ac/dc/ac converter can eliminate harmonics of output voltage effectively. The experimental results are in full agreement with theoretical analyses and simulation results.

V. CONCLUSIONS

The proposed novel inverter has such advantages as simpler structure, higher reliability, and higher performance. Moreover, because an MWR-SPWM technique is adopted to eliminate low harmonics in output voltage, not only the bulky dc filter capacitor is

greatly decreased or even removed, but also the size of output filter is reduced accordingly. The simulation and experimental results prove the validity of the analysis and the feasibility of the proposed MWR-SPWM technique.
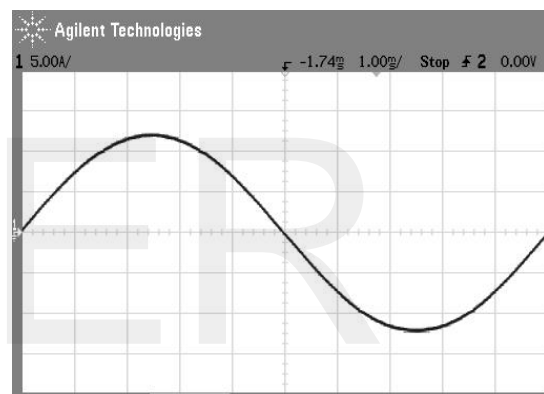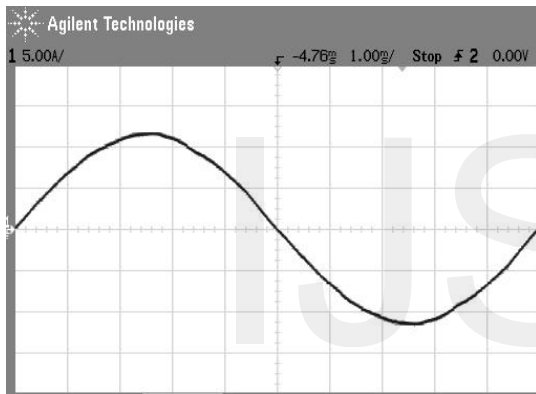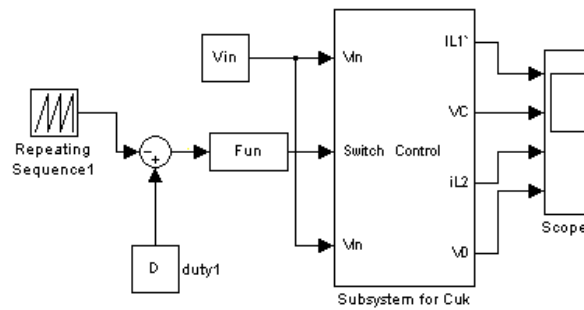


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Figure 4. Current waveform using general SPWM technique



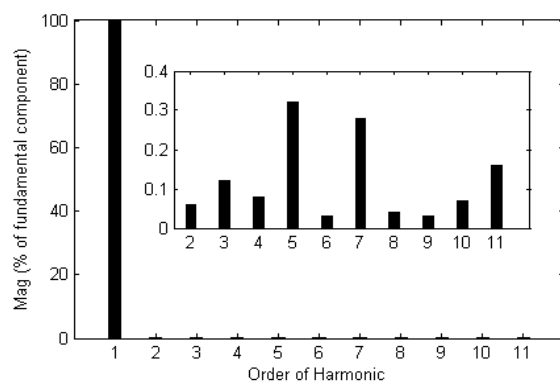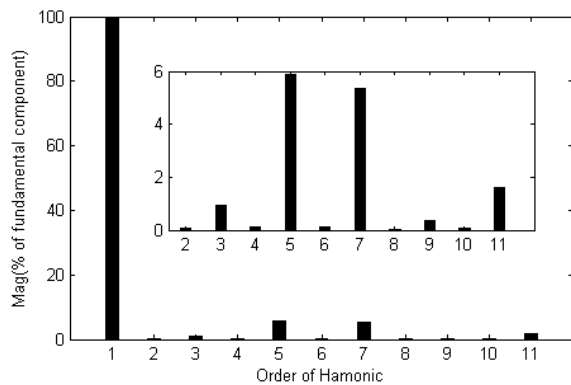Figure 5. Current waveform by using MWR-SPWM technique



Figure 6. The spectrum of using general SPWM technique



Figure 7. The spectrum of using MWR-SPWM technique

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